15th EUROMICRO CONFERENCE on DIGITAL SYSTEM DESIGN
DSD'2012
Architectures, Methods and Tools
Izmir, Turkey, 5-8 Sept. 2012

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Université de Valenciennes

Scope:

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed hardware/software system engineering, down to micro-architectures, digital circuits and VLSI techniques. It is a discussion forum for researchers and engineers from academia and industry working on state-of-the-art investigations, development and applications. It focuses on advanced circuit and system design and design automation concepts, paradigms, methods and tools, as well as on modern implementation technologies from full custom in nanometer technology nodes to FPGA and to multi-core infrastructures.
Compiler assisted ASIP, CMP, SMP, SMT, DSP-VLIW, GPU, platform based system design research results and application analysis and parallelization for embedded hardware and software design are welcome.
Design and Verification Languages and Standards, High Level Synthesis, Efficiency, Density, Signal Integrity, Testability, Timing Analysis and Timing Closure, Asynchronous Techniques, Reconfigurable Architectures, Power Consumption, Computational Power Speed and Performance, Productive Design Technology and Engineering Flows, Manufacturability, Cost, Reliability, Error Resilience, Complexity, or Process Variability issues, Modeling, Design Experiences are covered in DSD.

Main Topics

T1: (RC) Programmable/re-configurable/adaptable architectures
T2: (AP-HwSw) Application analysis and parallelization for embedded and high-performance design
T3: (SHES) System, hardware and embedded software design and automatic synthesis.
T4: (SoC & NoC) Systems-on-a-chip and networks-on-a-chip.
T5: (SMVT) System, hardware and embedded-software specification, modeling, verification and test.
T6: (APP) Applications of (embedded) digital systems.
T7: (ET) Important issues introduced by emerging technologies.

Special Sessions

SS1: (FDR) Flexible Digital Radio.
SS2: (MSDA) Multicore Systems: Design and Applications
SS3: (DTDS) Dependability and Testing of Digital Systems
SS4: (FTDSD) Fault Tolerance in Digital System Design
SS5: (SEO-WN) System-level Energy Optimization and Wireless Sensor Networks
SS6: (AHSA) Architectures and Hardware for Security Applications
SS7: (MoRPS) Monitoring and Reconfiguration of Parallel Systems
SS8: (DCPS) Design of Heterogeneous Cyber-Physical Systems
SS9 (EPDSD): European Projects in Digital System Design (invited papers only)

Submission Guidelines

Prospective authors are encouraged to submit their manuscripts for review electronically through the following web page (http://www.easychair.org/conferences/?conf=dsd2012) or by sending the paper to the Session Chair via email (smail.niar@univ-valenciennes.fr) only if an unexpected web access problem is encountered) before the deadline for submission.
Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the required CPS Format: single-spaced, double column, A4/US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the submitted manuscript, references included.
The Conference Publishing Services (CPS) publishes the DSD Proceedings, which are submitted to the IEEE Xplore Digital Library.
An extended version of the best papers will be published in a special issue of the ISI-indexed ***"Microprocessors and Microsystems: Embedded Hardware Design"*** **journal**, printed by Elsevier Ltd.

Important Dates

- **Submission of papers: March 19th, 2012**
- Notification of acceptance: **May 1st, 2012**
- Camera ready papers: **May 28th, 2012**

Web Links

- DSD'12 web page:
- ***www.univ-valenciennes.fr/dsd2012/***
- Euromicro web page:
  http://www.euromicro.org